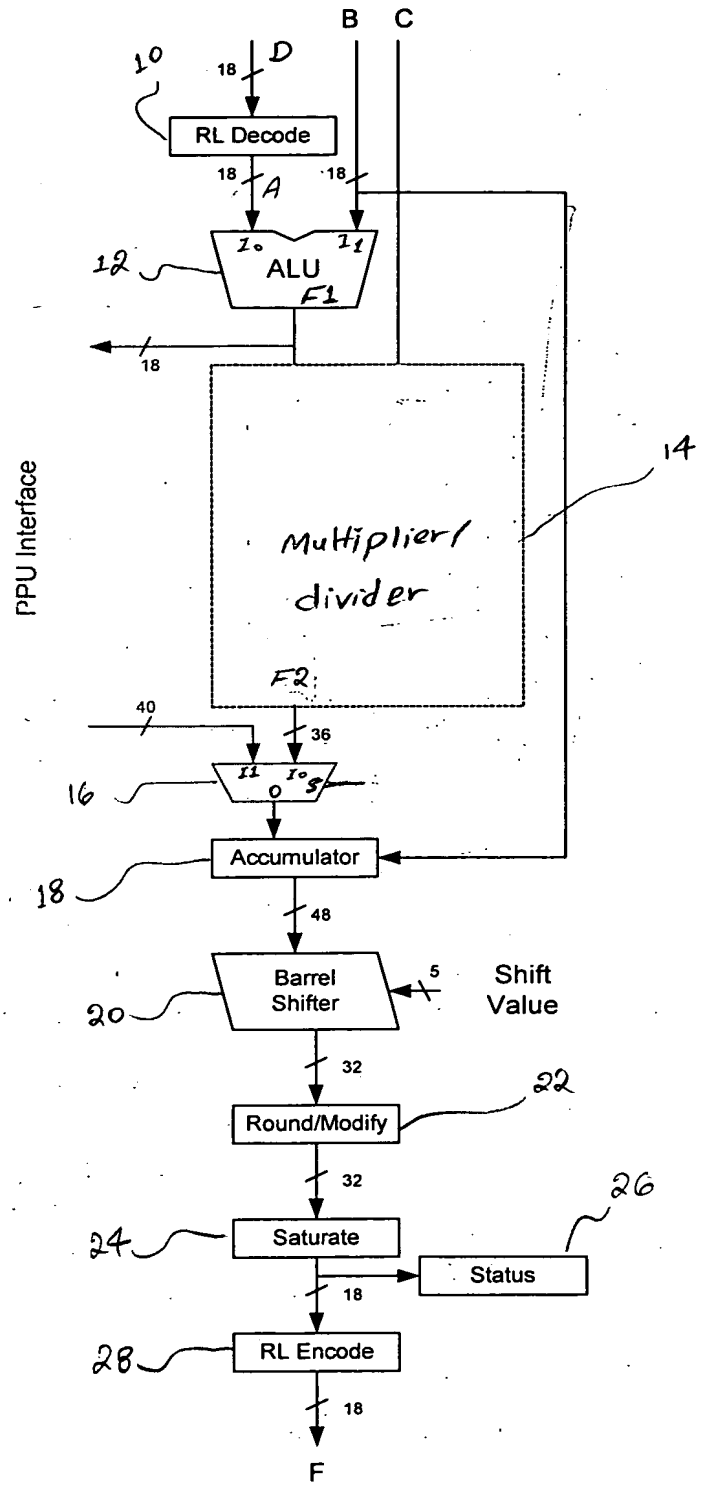
100
B
C
10
18
D
RL Decode
18
A
18
12
I0
I1
ALU
F1
18
PPU Interface
14
Multiplier/
divider
F2
40
36
16
I1
I0
S
0
Accumulator
18
48
Barrel
Shifter
5
Shift
Value
20
32
22
Round/Modify
32
26
24
Saturate
Status
18
28
RL Encode
18
F

Fig. 1

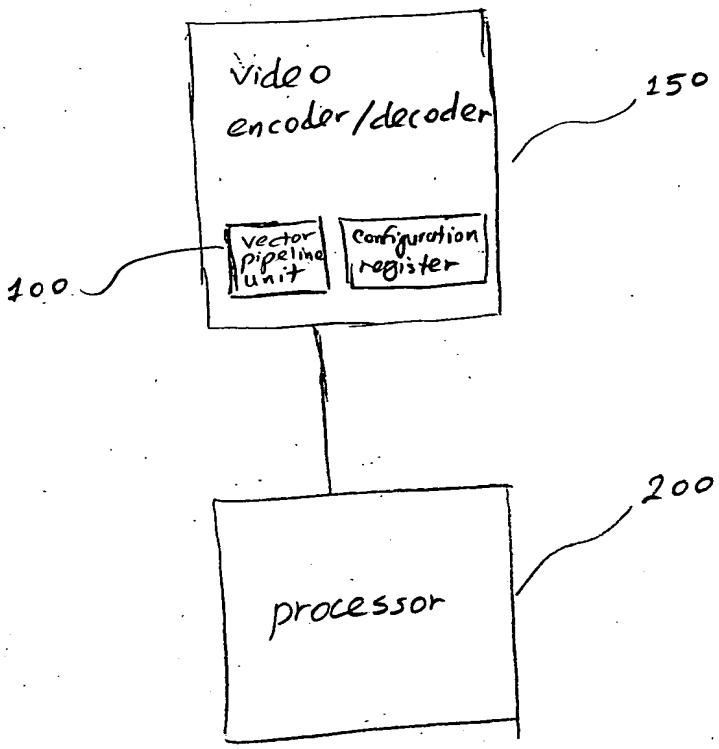


Fig. 2

DOCKET NO. 2003-0107

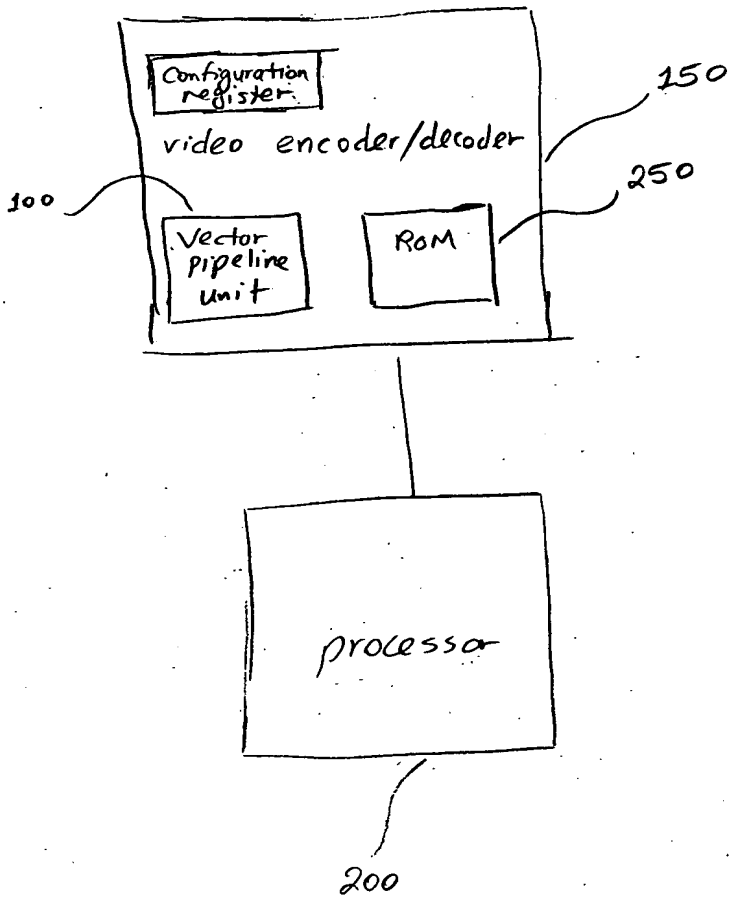


Fig. 3

09/08/2004, EAST Version: 1.4.1